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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP04/17883 12/01/2004

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2003-407852 12/05/2003  
 JAPAN 2004-115132 04/09/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 29	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

**ADDRESS**

23370

**TITLE**

REACTANCE ADJUSTER, TRANSCEIVER AND TRANSMITTER USING THE REACTANCE ADJUSTER, SIGNAL PROCESSING CIRCUIT SUITABLE FOR USE IN THE REACTANCE ADJUSTER, THE TRANSCEIVER, AND THE TRANSMITTER, REACTANCE ADJUSTING METHOD, TRANSMITTING METHOD, AND RECEIVING METHOD

FILING FEE RECEIVED 4930	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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